

REGULATING A MAGNETIC MEMORY CELL WRITE CURRENT

Abstract

The invention includes an apparatus and method for regulating a magnetic memory cell write current. The method includes modifying a magnetic memory cell write current by summing a write current offset to the magnetic memory cell write current, and determining whether writing to a magnetic memory cell with the modified magnetic memory cell write current results in a write error condition. If a write error condition exists, then the method includes incrementing the magnetic memory cell write current, or decrementing the magnetic memory cell write current, until the write error condition is eliminated.